



Manufacturer's Statement for Anti-islanding Method

Dear Customer:

The purpose of this letter is to declare the anti-islanding methodology used by the 18kPV-12LV inverter.

This inverter meets the "IEEE STD. 1547 and 1547.1" and "UL STD. 1741, 1741 SB" standard and has obtained the corresponding certificates.

The **18kPV-12LV** inverter uses method (a) of anti-islanding listed below:

- (a) **Shifting the frequency of the inverter away from nominal conditions in the absence of a reference frequency (frequency shift);**
- (b) Allowing the frequency of the inverter to be inherently unstable in the absence of a reference frequency (frequency instability);
- (c) Periodically varying the output power of the inverter (power variation); and
- (d) Monitoring for sudden changes in the impedance of the grid by periodically injecting a current pulse (current injection)

For any questions, please contact EG4 Electronics LLC at support@eg4electronics.com or by phone at (903) 609-1988.

Regards,

A handwritten signature in black ink, appearing to read 'Eric Schlee'.

Eric Schlee

EG4 Electronics, LLC

Compliance Engineer